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A method of hardware implementation of membrane computing architectures for mobile robot control

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Abstract

This paper proposes a method of hardware implementation of the membrane computing architecture for the control of a mobile robot. The basic idea is to use in the development of control systems the models of functional description of living cells, which should ensure a process of design, modeling and implementation based on cognitive models. The implementation algorithm presents a sequence of operations involving: functional development and modeling of Computing Cells, development and modeling of the topology of the membrane computing system (P-System), their implementation in hardware description languages (AHDL), and configuration of the FPGA circuit for realization of the control system. For validation and analysis of the performance of the Computing Cells, Petri Net models are used, which ensure the identification of the concurrent processes while maintaining a maximum parallelism. Functional testing of membrane computing models was performed based on the Quartus II development environment, the AHDL hardware description language, and the Altera DEO Board.

Keywords: *membrane computing, mobile robot control, hardware implementation*

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